



MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(UGC-AUTONOMOUS INSTITUTION)



Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi
NAAC Accredited with A+ Grade, NIRF India Rankings 2025 - Band: 201-300(Engg.)
NBA Accredited - B.Tech. (CIVIL, CSE, CST, ECE, EEE, MECH), MBA & MCA

www.mits.ac.in

CIRCULAR

Date: 16.12.2025

Madanapalle

All the M.Tech II Year I Semester 2024 Batch (R24) Regular M.Tech students are hereby informed that the External Practical Exams will be conducted on 22.12.2025 as per the time table given below.

M.Tech (VLSI Design & Embedded Systems) II Year I Semester (R24)Regular : 2024 Admitted Batch

QNX Embedded Real Time Operating System 24VESP601

Date	Time	Roll No.S	No.of Students	Lab Slot
22.12.2025	09:30AM-12:30 PM	24691D6901-24691D6907, 24691D6909-24691D6915	14	LB-202

Note : (1) **Detained & Unregistered** students of the current semester are not eligible to write the above examinations, if in case the examination is conducted for any such student, it stands cancelled without any prior intimation

(2) if any discrepancies are found, the same may be informed to the examination section

Geo Nadarajah

CONTROLLER OF EXAMINATIONS

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MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
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P. J. R. S. S.

PRINCIPAL

Principal
Madanapalle Institute of
Technology & Science
MADANAPALLE